

SYSTEM 1

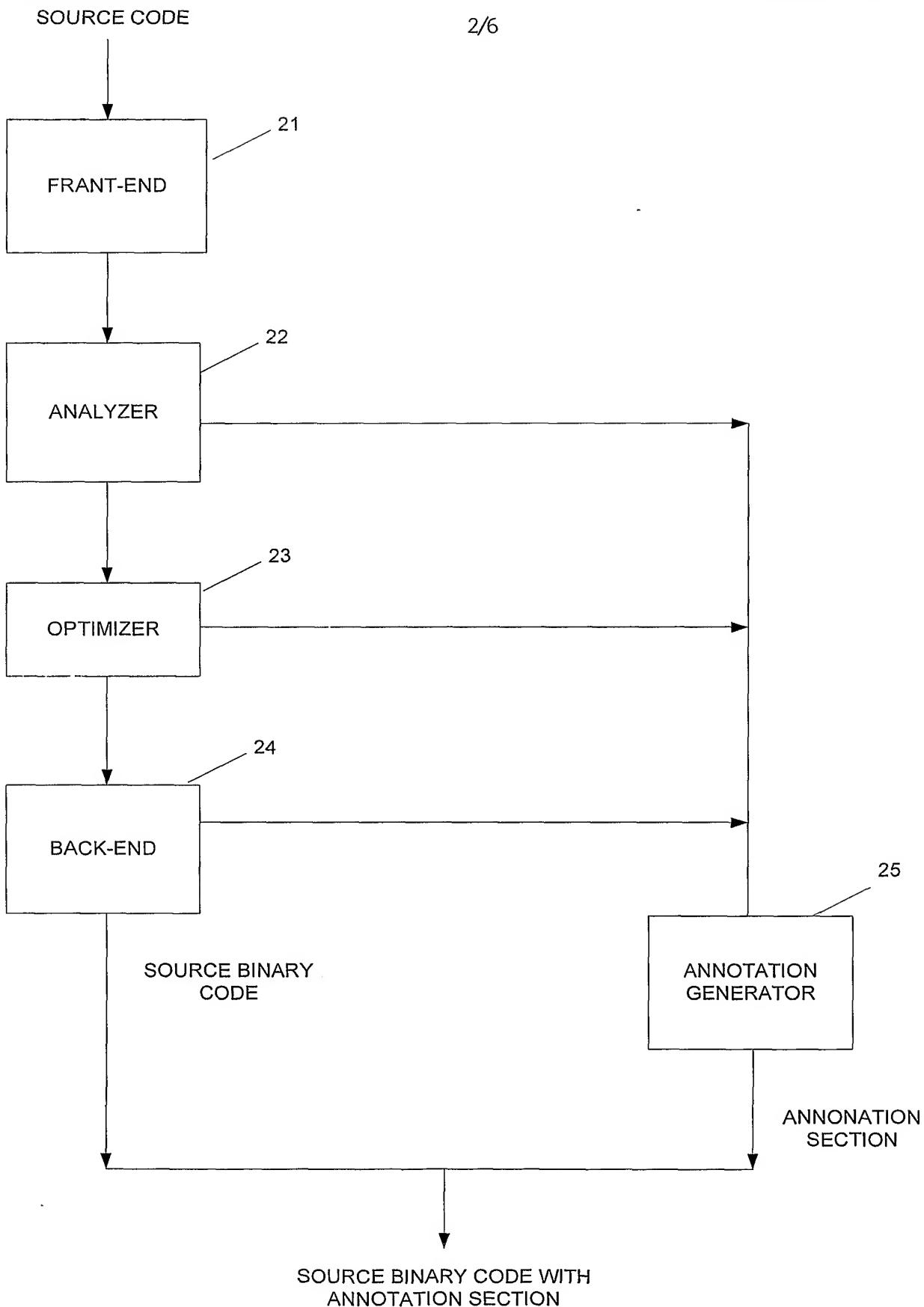
1/6

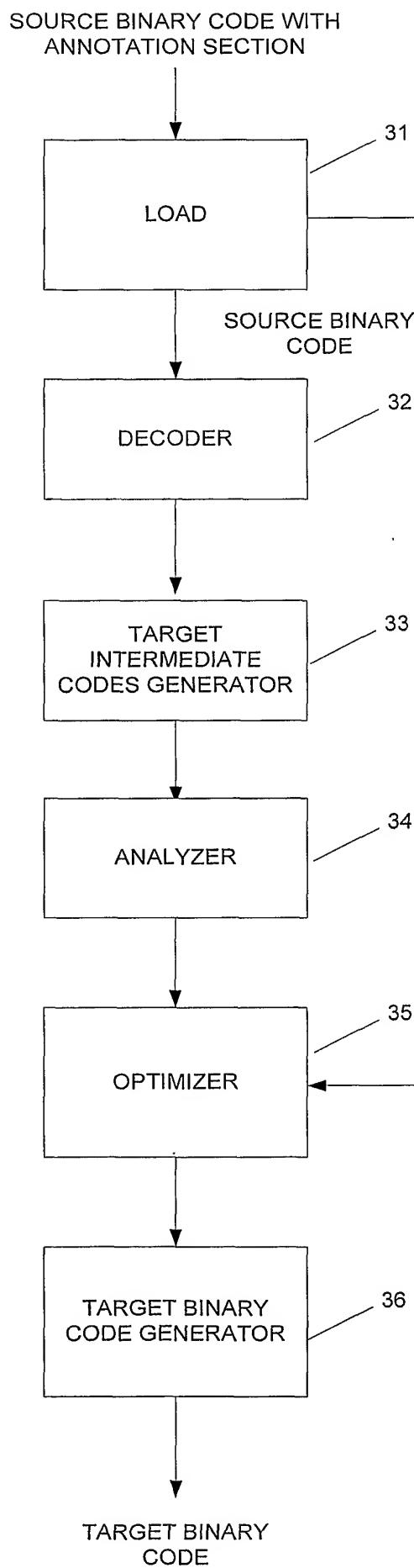
SOURCE COMPUTING
DEVICE 100SOURCE
PROCESSOR
110TARGET COMPUTING
DEVICE 200TARGET
PROCESSOR
210SOURCE
MEMORY 100SOURCE
CHIPSET 130TARGET
I/O 220

TARGET MEMORY 200

TARGET
BINARY
CODES 202TRANSLATOR
201TARGET
CHIPSET 230SOURCE
I/O 120SOURCE
CODES 101COMPILER
102

FIG. 1

**FIG. 2**



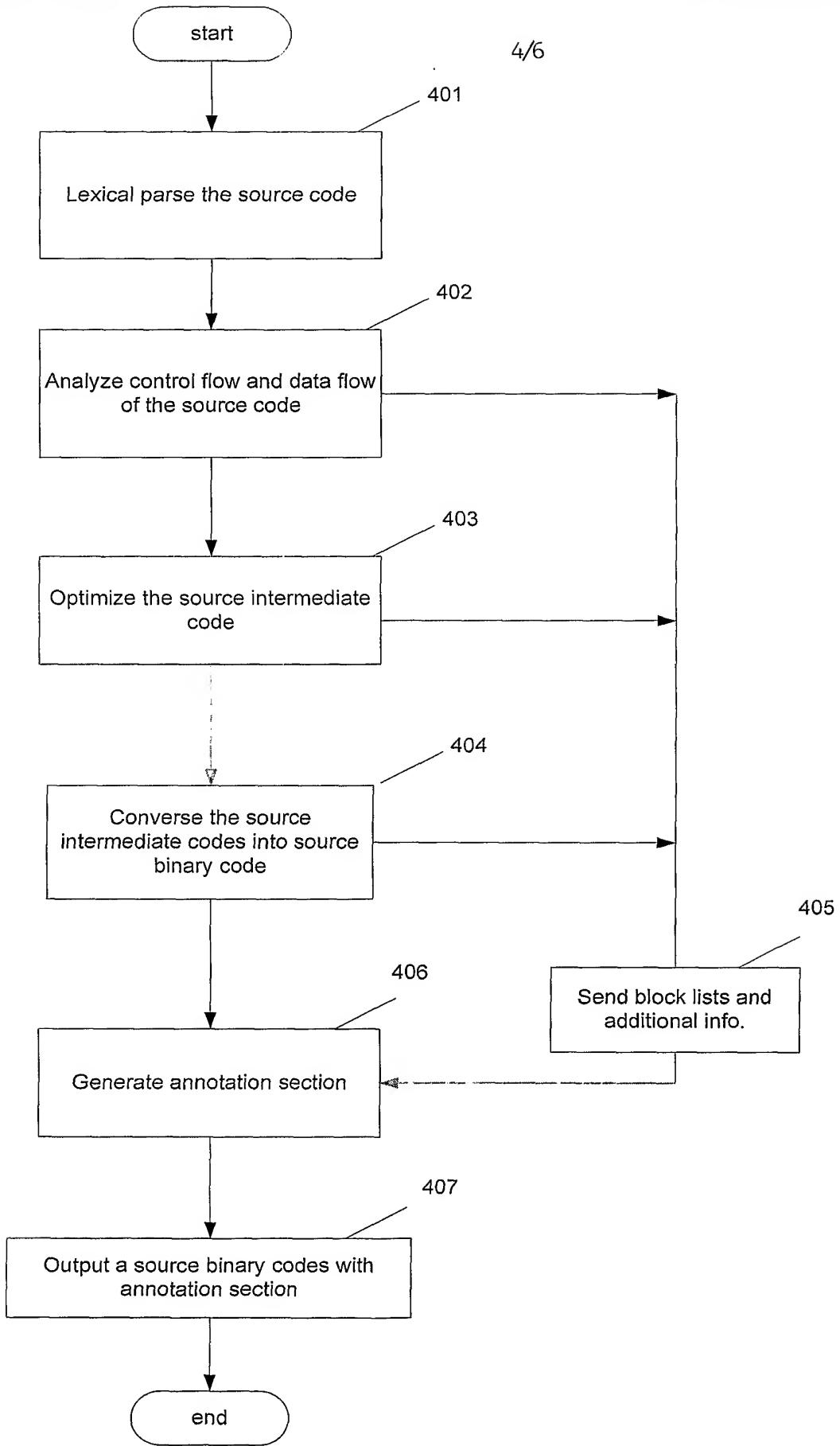
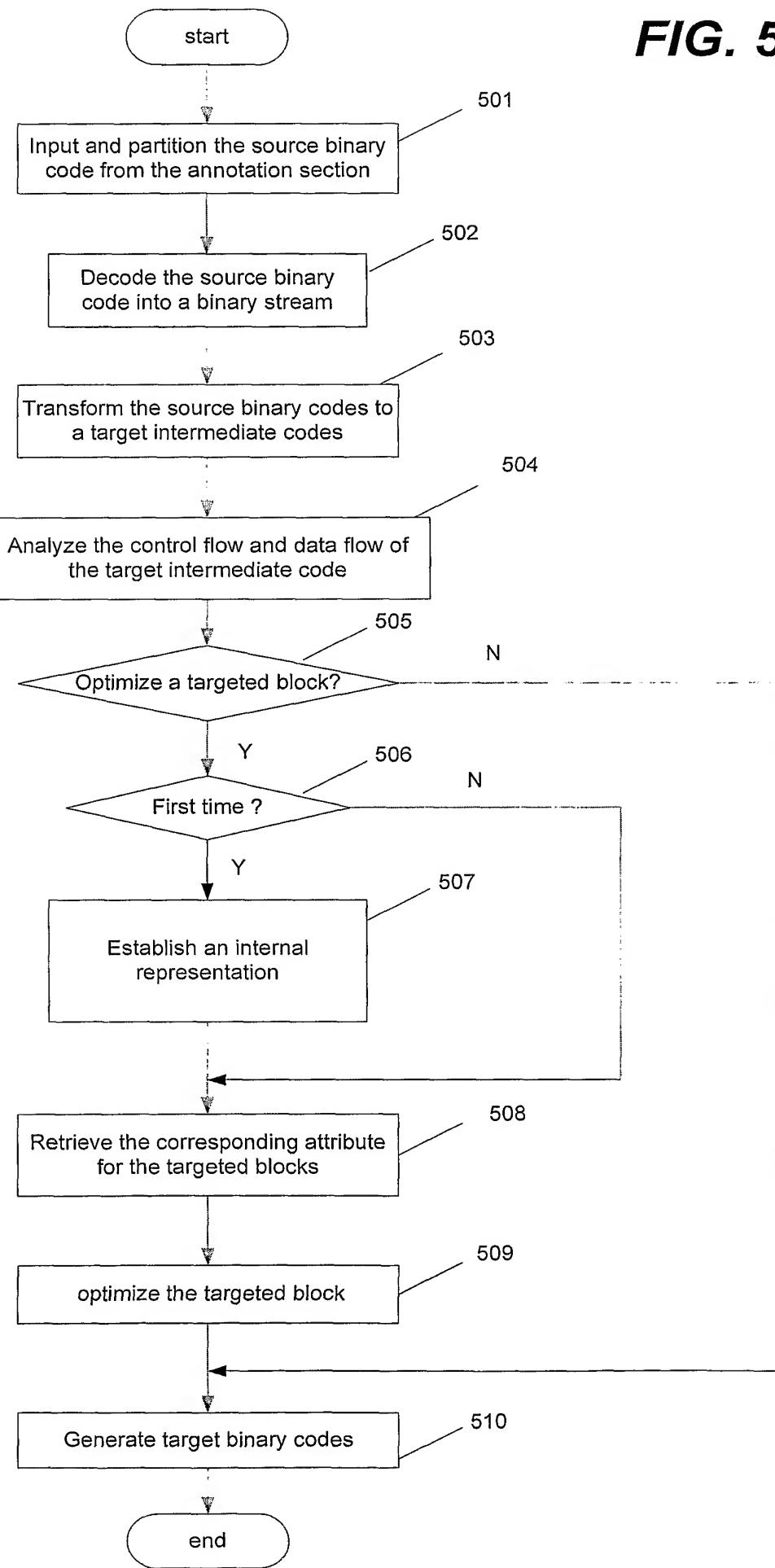
**FIG. 4**

FIG. 5

Magic Value
Table size
Region start address
Region size
Attribute information
Number of scope
Reserved bytes
Scope start offset
Scope end offset
Scope size
Attribute information
Reserved bytes